

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

**O.I.P.E.**

**PATENT DATE**

SCANNED

94

APPLICATION NO. 09/660198	CONT/PRIOR F	CLASS 257	SUBCLASS 347	ART UNIT 2811	EXAMINER Lolke Meeus
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TS Keiko Kawamura

## APPLICANTS

**TITLE**

Transistor capable reducing on-resistance and improving withstand voltage and method of manufacturing the same

PTO-2040  
12/89[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>  _____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Primary Examiner)		<b>ISSUE FEE</b>  <div style="display: flex; justify-content: space-between;"> <span>Amount Due</span> <span>Date Paid</span> </div>	
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